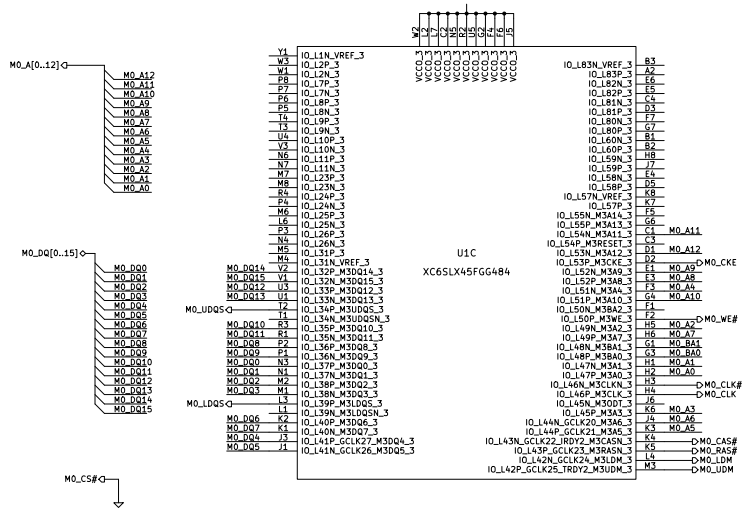
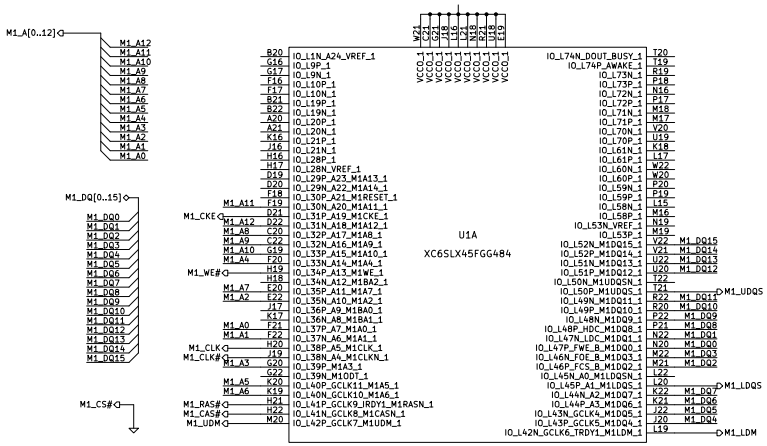
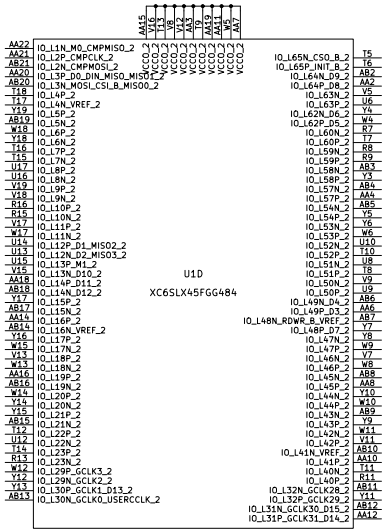

U1C
XC6SLX45FGG484
M0_A[0..12]
M0_DQ[0..15]
M0_UDQS
M0_LDQS
M0_CS#
M0_CKE
M0_WE#
M0_CLK#
M0_CLK
M0_CAS#
M0_RAS#
M0_LDM
M0_UDM


U1A
XC6SLX45FGG484
M1_A[0..12]
M1_DQ[0..15]
M1_CKE
M1_WE#
M1_CLK
M1_CS#
M1_RAS#
M1_CAS#
M1_UDM
M1_UDQS
M1_LDQS
M1_LDM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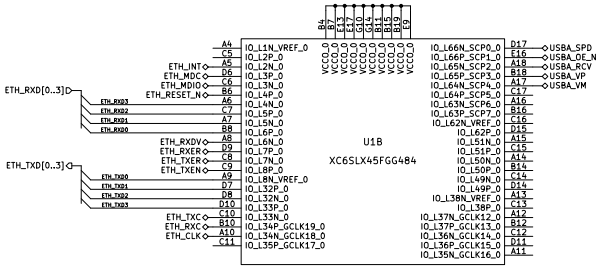
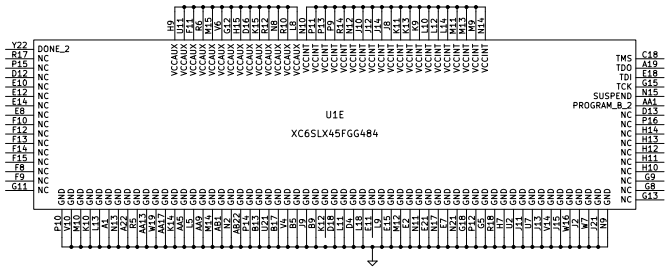
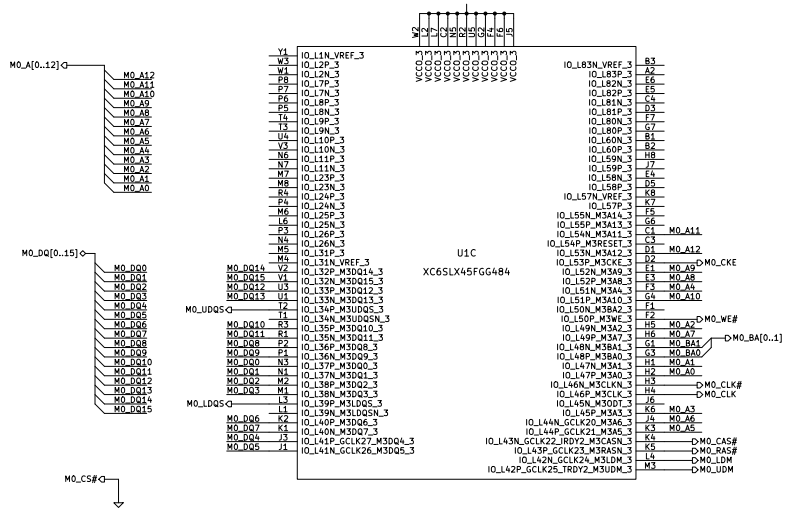
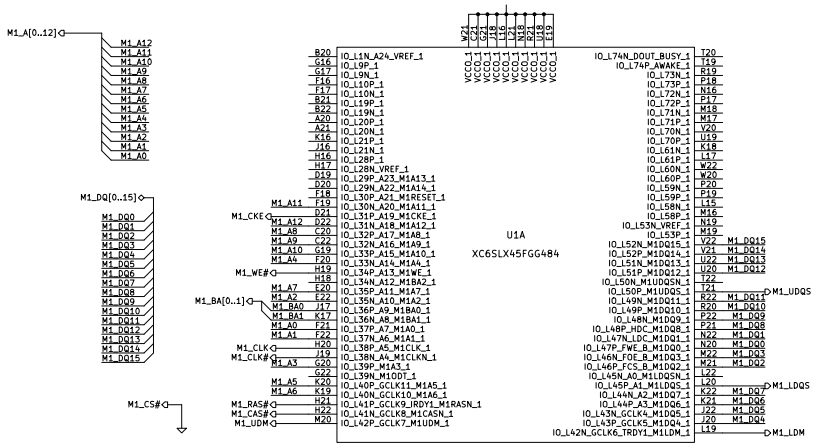
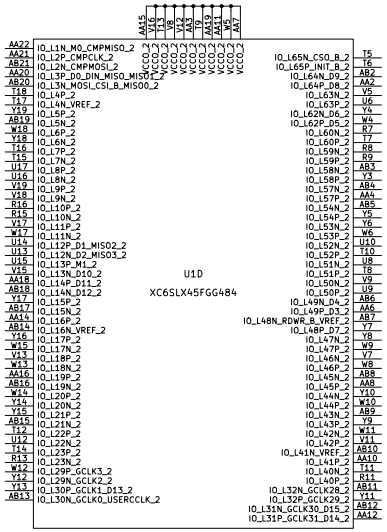
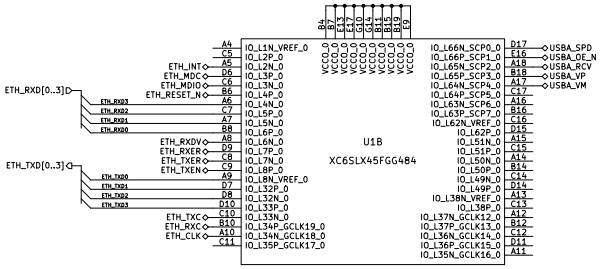

U1B
XC6SLX45FGG484
ETH_RXD[0..3]
ETH_TXD[0..3]
ETH_INT
ETH_MDC
ETH_MDIO
ETH_RESET_N
ETH_RXDV
ETH_RXER
ETH_TXER
ETH_TXEN
ETH_TXC
ETH_RXC
ETH_CLK
USBA_SPD
USBA_OE_N
USBA_RCV
USBA_VP
USBA_VM

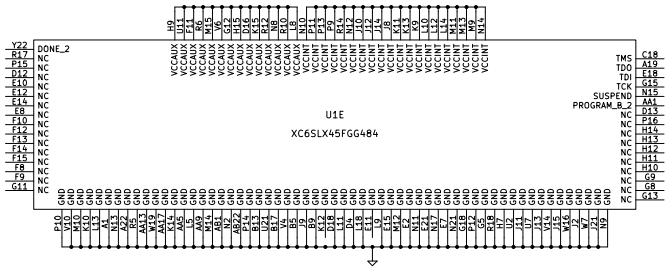

U1E
XC6SLX45FGG484
DONE_2
TMS
TDO
TDI
TCK
SUSPEND
PROGRAM_B_2


U1C
XC6SLX45FGG484
M0_A[0..12]
M0_DQ[0..15]
M0_CS#
M0_UDQS
M0_LDQS
M0_CKE
M0_WE#
M0_BA[0..1]
M0_CLK#
M0_CLK
M0_CAS#
M0_RAS#
M0_LDM
M0_UDM


U1A
XC6SLX45FGG484
M1_A[0..12]
M1_DQ[0..15]
M1_CKE
M1_WE#
M1_BA[0..1]
M1_CLK
M1_CLK#
M1_CS#
M1_RAS#
M1_CAS#
M1_UDM
M1_UDQS
M1_LDQS
M1_LDM


U1D
XC6SLX45FGG484


U1B
XC6SLX45FGG484
ETH_RXD[0..3]
ETH_TXD[0..3]
ETH_INT
ETH_MDC
ETH_MDIO
ETH_RESET_N
ETH_RXDV
ETH_RXER
ETH_TXER
ETH_TXEN
ETH_TXC
ETH_RXC
ETH_CLK
USBA_SPD
USBA_OE_N
USBA_RCV
USBA_VP
USBA_VM


U1E
XC6SLX45FGG484
DONE_2
TMS
TDO
TDI
TCK
SUSPEND
PROGRAM_B_2